

Figure 1 is a block diagram illustrating a computer system architecture. The main system unit (1) contains a CPU (3), memory (4), and various input/output devices (5, 10, 12). It also features a slot (6) for expansion modules. Two peripheral units, (2) and (9), are shown connected to the main system unit. Unit (2) includes a slot (7) for expansion modules (8) and a connector (11). Unit (9) includes a slot (7) for expansion modules (8) and a connector (13). The diagram shows how these units can be interconnected to form a larger system.

CPU

FIG 2

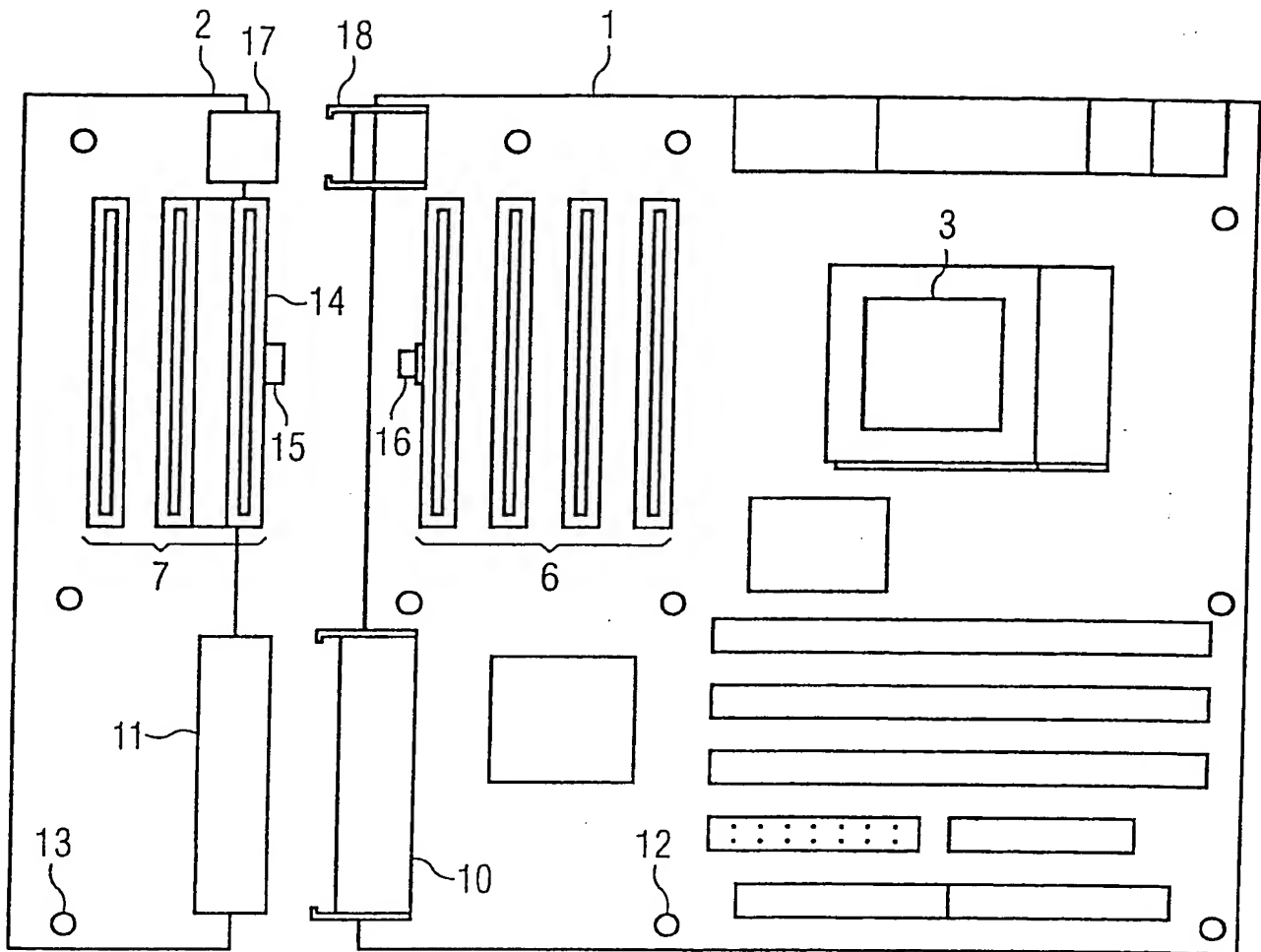


FIG 3

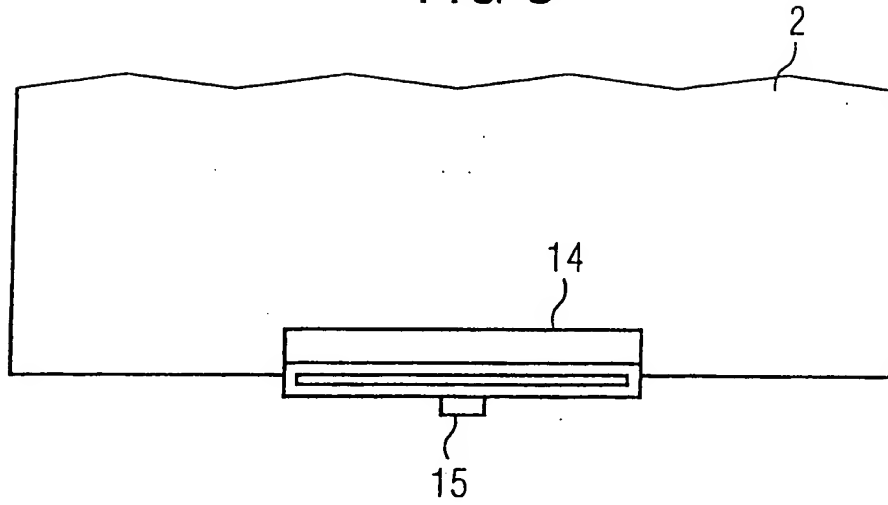


FIG 4

